



PATENT  
8040-1016

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of:

Toshiyuki HIROTA

Confirmation No. 4866

Serial No. 09/939,672

GROUP 2878

Filed August 28, 2001

Examiner Thanh X. Luu

SEMICONDUCTOR DEVICE PACKAGE HAVING A SWITCHER  
CONNECTING PLURAL PROCESSING ELEMENTS

AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 21, 2004

Sir:

Responsive to the Official Action of October 21, 2003,  
please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing  
of claims which begins on page 2 of this paper.

**Remarks** begin on page 7 of this paper.

*Cortright*, 165 F.3d 1353, 1359, 49 USPQ2d 1464, 1468 (Fed. Cir. 1999).

One of ordinary skill in the art in the large scale integration (LSI) package art would not interpret the claims to encompass an automated teller machine and thus the proposed combination of references would not be obvious to one having ordinary skill in the art. The comments above regarding claim 4 are equally applicable to claims 9-12 which provide a plurality of semiconductor chips.

New claim 15 provides for an LSI semiconductor device having peripheral dies and a single switcher die. New claim 16 provides that the light is confined by a sealing resin. The combination of references does not teach these features.

New claims 17 and 18 are directed to the embodiments in Figures 4 and 5. The automated teller machine of COUTTS et al. and the single chip of disclosed prior art Figure 1 do not teach or suggest plural stacked packages that form a system LSI.

In view of the present amendment and the foregoing remarks, it is believed that the present application has been placed in condition for allowance. Reconsideration and allowance are respectfully requested.

Please charge the fee of \$86 for the one extra independent claim added herewith to Deposit Account No. 25-0120.